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Cortex M3 User Manual

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Documentation - Arm Developer

Cortex-M3InstructionSet. Table1-1.Cortex-M3Instructions(continued) Mnemonic Operands BriefDescription Flags SeePage ORR, ORRS {Rd,} Rn, Op2 LogicalOR N,Z,C 43 POP reglist Popregistersfromstack - 37 PUSH reglist Pushregistersontostack - 37 RBIT Rd, Rn Reversebits - 55

Cortex-M3 Instruction Set Technical User's Manual

1.2 About the EFM32 Cortex-M3 processor and core peripherals The EFM32 Cortex™ -M3 processor is a high performance 32-bit processor designed for the microcontroller market. It offers significant benefits to developers, including: • outstanding processing performance combined with fast interrupt handling

EFM32 Cortex-M3 Reference Manual - Silicon Labs

3.5.1 Syntax.....64 3.5.2 Operation.....64

Cortex-M3/M4F Instruction Set Technical User's Manual (Rev. A)

Cortex-M3 DesignStart Eval provides developers an easy way to develop and simulate SoC designs based on the ARM Cortex-M3 processor. It allows a system designer to design and test on a simulator and then proceed with hardware prototyping using an FPGA.

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UM10430 LPC18xx ARM Cortex-M3 microcontroller

1.3 About the STM32 Cortex®-M3 processor and core peripherals The Cortex-M3 processor is built on a high-performance processor core, with a 3-stage pipeline Harvard architecture, making it ideal for demanding embedded applications. The processor delivers exceptional power efficiency through an efficient instruction set and

PM0056 Programming manual - STMicroelectronics

Cortex-M3, 32-bit, USB, Ethernet, CAN, I2S, Microcontroller Abstract LPC176x/5x user manual. ... User manual Rev. 4. 1 — 19 December 2016 2 of 851 NXP Semiconductors UM10360 LPC17xx user manual Revision history Rev Date Description 4.1 20161219 LPC176x/5x user manual Modifications:

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STM32F103C8 - Mainstream Performance line, Arm Cortex-M3 MCU with 64 Kbytes of Flash memory, 72 MHz CPU, motor control, USB and CAN, STM32F103C8T6TR, STM32F103C8T6, STM32F103C8T7TR, STM32F103C8T7, STMicroelectronics

STM32F103C8 - Mainstream Performance line, Arm Cortex-M3 ...

Cortex-M3, Cortex-M4, and Cortex-M7. The menu Peripherals - Core Peripherals opens dialogs that show the status and features of the device core. The following dialogs are available for devices based on Cortex-M3, Cortex-M4, and Cortex-M7 processors: Nested Vector Interrupt Controller; System Control and Configuration; System Tick Timer; Fault ...

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UM10375 LPC1311/13/42/43 User manual - NXP Semiconductors

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User manual Rev. 2 — 19 August 2010 3 of 840 1.1 Introduction The LPC17xx is an ARM Cortex-M3 based microcontroller for embedded applications requiring a high level of integration and low power dissipation. The ARM Cortex-M3 is a next generation core that offers system enhancements such as modernized debug

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the powerful 32-bit ARM Cortex-M3, innovative low energy techniques, short wake-up time from energy saving modes, and a wide selection of peripherals, the EFM32TG microcontroller is well suited for any battery operated application, as well as other systems requiring high performance and low-energy consumption, see Figure 3.1 (p. 7) . 3.2 Features